

SC 12009 TGA J



(19) RU (11) 2 137 295 (13) C1
(51) Int. Cl. 6 H 03 K 19/00

RUSSIAN AGENCY
FOR PATENTS AND TRADEMARKS

(12) ABSTRACT OF INVENTION

(21), (22) Application: 96122569/09, 02.12.1996

(46) Date of publication: 10.09.1999

(98) Mail address:
117279, Moskva, ul.Miklukho-Maklaja 55a, AO
TsPU "PATIS", Beljavskoj N.B.

(71) Applicant:
Kushner Jurij Konstantinovich

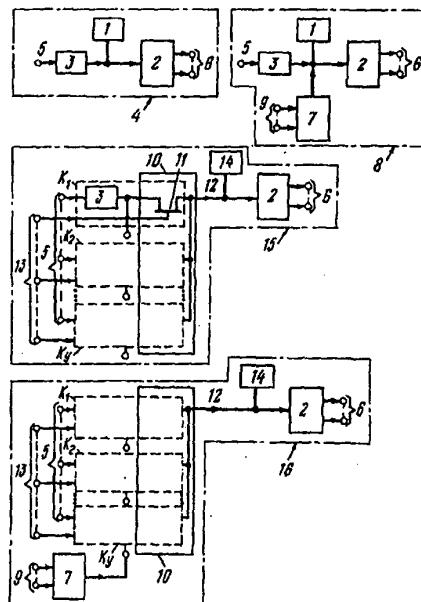
(72) Inventor: Kushner Ju.K.

(73) Proprietor:
Kushner Jurij Konstantinovich

(54) METHOD FOR LINKING INDIVIDUALLY DESIGNED LOGIC CIRCUITS IN CUSTOM LARGE-SCALE
INTEGRATED CIRCUIT

(57) Abstract:

FIELD: computer engineering. SUBSTANCE:
method involves formation of set of
individually designed logic circuits of
simplest elements which are coincidence
hardware assemblies of logic circuits,
installation, arrangement, and tracing
configurations of simplest elements. EFFECT:
improved functions, configurations, and
parameters of individually designed logic
circuits. 1 dwg



R U 2 1 3 7 2 9 5 C 1

R U 2 1 3 7 2 9 5 C 1